

Figure 1

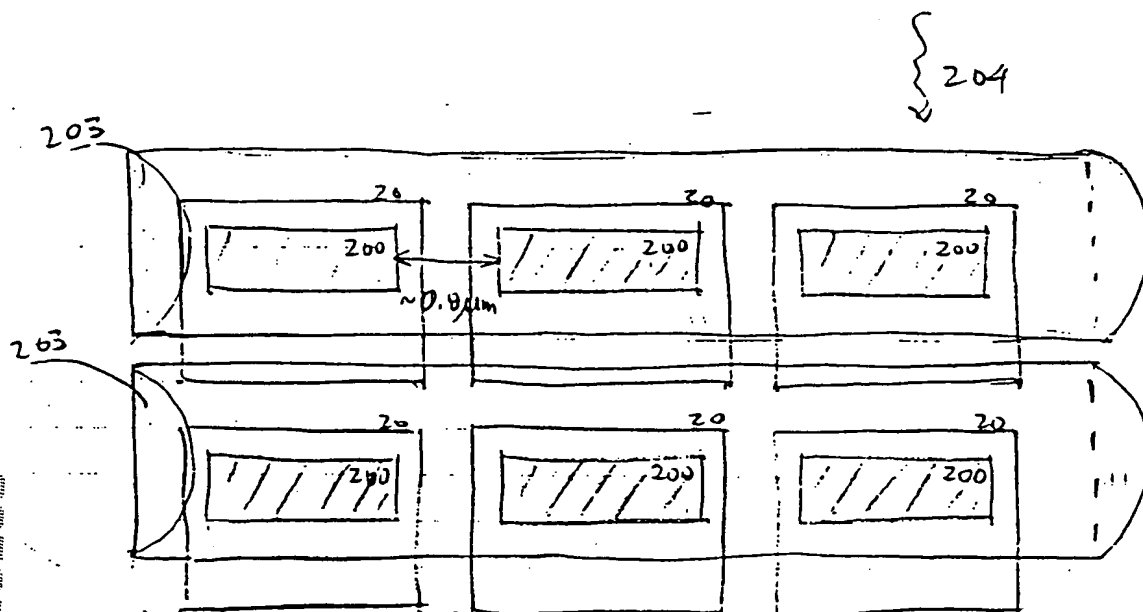
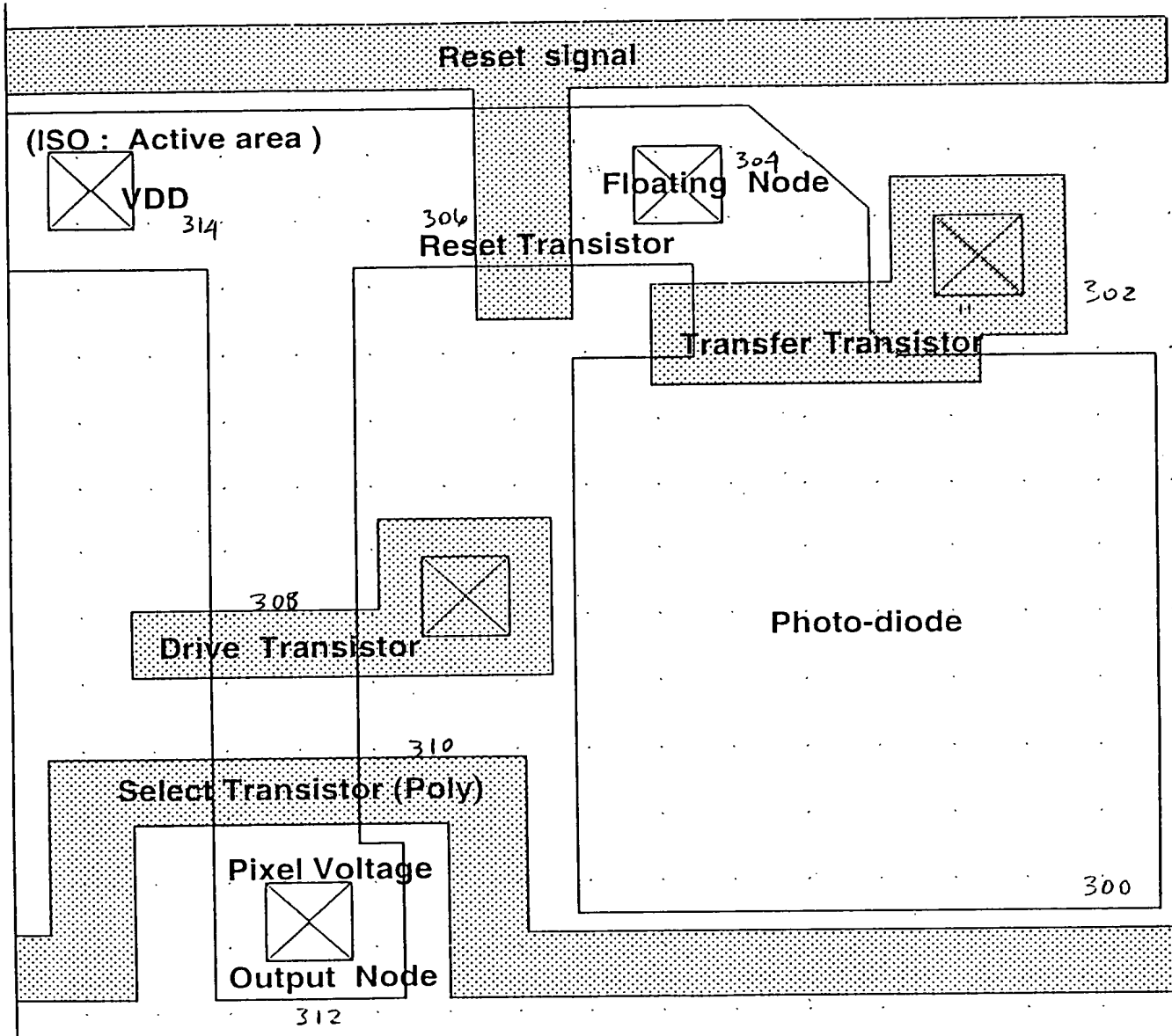


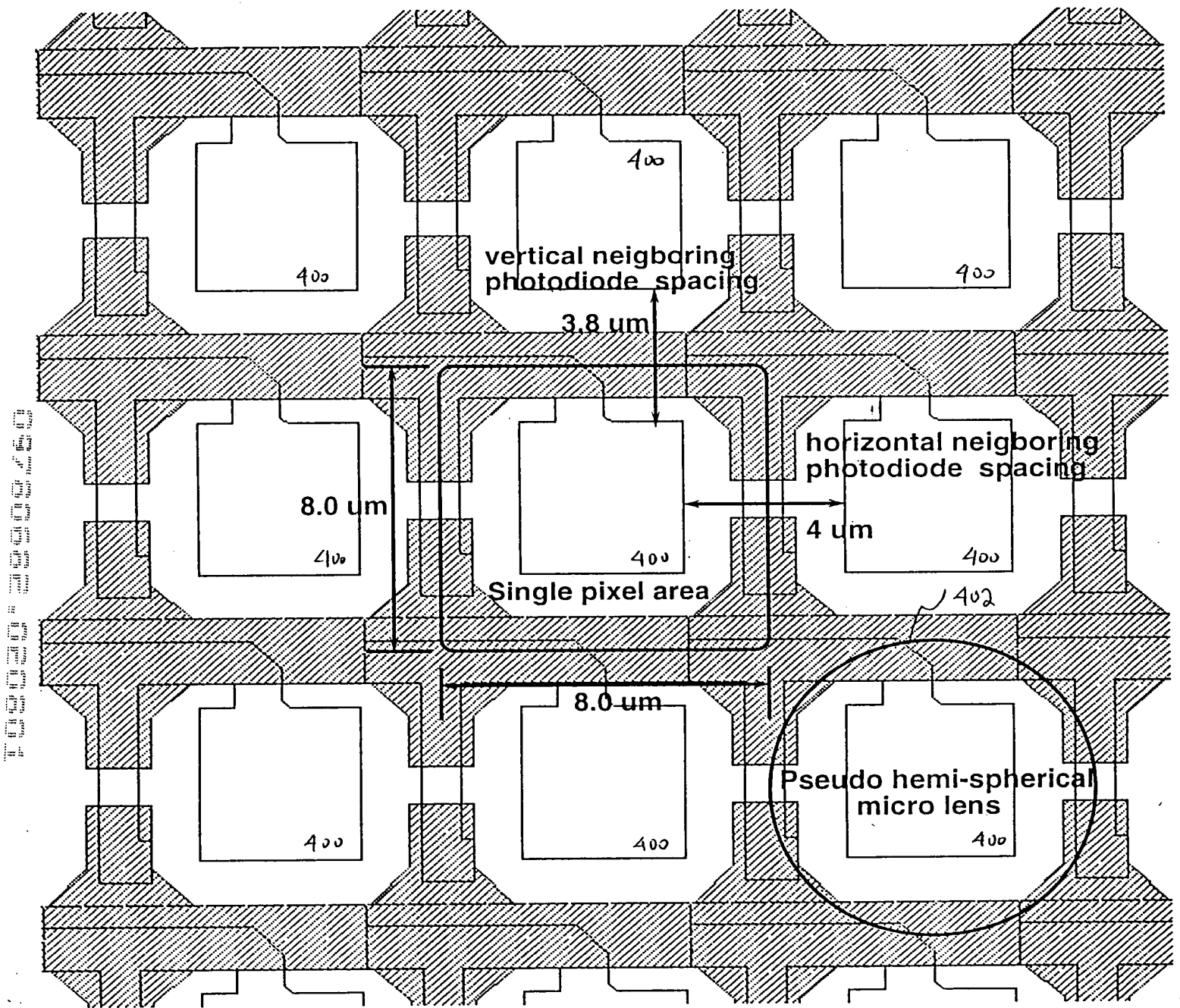
Figure 2

FIG. 3 is a schematic diagram of a pixel circuit in accordance with the present invention. The pixel circuit includes a photo-diode 300, a floating node 304, a reset transistor 306, a transfer transistor 302, a drive transistor 308, a select transistor (poly) 310, a pixel voltage output node 312, and a VDD supply 314. The reset transistor 306 is controlled by a reset signal. The transfer transistor 302 is controlled by a select signal. The drive transistor 308 is controlled by a pixel voltage signal. The select transistor (poly) 310 is controlled by a select signal. The pixel voltage output node 312 is connected to the output of the drive transistor 308. The floating node 304 is connected to the output of the transfer transistor 302. The photo-diode 300 is connected to the floating node 304. The VDD supply 314 is connected to the output of the reset transistor 306. The reset signal is applied to the gate of the reset transistor 306. The select signal is applied to the gate of the transfer transistor 302 and the select transistor (poly) 310. The pixel voltage signal is applied to the gate of the drive transistor 308.



301

Figure 3



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Figure 4

Figure 5

